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Edition), Pearson Prentice Hall, 2008. • Stephen Brown and Zvonko Vranesic, Fundamentals of Digital Logic with VHDL Design, McGraw Hill, 2000, • Handouts describing tools and equipment in the lab will be handed out in the class and/or posted on the course web-site throughout the semester. It is your duty to attend the classes and regularly check ...

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The standard representation of these three logic building blocks is shown in Figure C.2.1. Rather than draw inverters explicitly, a common practice is to add “ bubbles ” to the inputs or outputs of a gate to cause the logic value on that input line or output line to be inverted. For example, Figure C.2.2 shows the logic diagram for the function

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Appendix C The Basics of Logic Design

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Featuring a strong emphasis on the fundamentals underlying contemporary logic design using hardware description languages, synthesis, and verification, this book focuses on the ever-evolving applications of basic computer design concepts with strong connections to real-world technology.

Based on the book Computer Engineering Hardware Design (1988), which presented the same combined treatment of logic design, digital system design and computer design basics. Because of its broad coverage of both logic and computer design, this text can be used to provide an overview of logic and computer hardware for computer science, computer engineering, electrical

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engineering, or engineering students in general.

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Fundamentals of Digital Logic and Microcomputer Design, has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers. In this Fifth Edition, the author focuses on computer design at three levels: the device level, the logic level, and the system level. Basic topics are covered, such as number systems and Boolean algebra, combinational and sequential logic design, as well as more advanced subjects such as assembly language programming and microprocessor-based system design. Numerous

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examples are provided throughout the text. Coverage includes: Digital circuits at the gate and flip-flop levels
Analysis and design of combinational and sequential circuits
Microcomputer organization, architecture, and programming concepts
Design of computer instruction sets, CPU, memory, and I/O
System design features associated with popular microprocessors from Intel and Motorola
Future plans in microprocessor development
An instructor's manual, available upon request
Additionally, the accompanying CD-ROM, contains step-by-step procedures for installing and using Altera Quartus II software, MASM 6.11 (8086), and 68asmsim (68000), provides valuable simulation results via screen shots.

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Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems.

Digital Design and Computer Architecture: ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of this book, readers will be able to build their own microprocessor and will have

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a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing an ARM processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or

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students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs)—SystemVerilog and VHDL—which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices

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such as LCDs, Bluetooth radios, and motors. The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools, lecture slides, laboratory projects, and solutions to exercises.

This book presents the basic concepts used in the design and analysis of digital systems and introduces the principles of digital computer organization and design.

For courses on digital design in an Electrical Engineering, Computer Engineering, or Computer Science department. Digital Design, fifth edition is a

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modern update of the classic authoritative text on digital design. This book teaches the basic concepts of digital design in a clear, accessible manner. The book presents the basic tools for the design of digital circuits and provides procedures suitable for a variety of digital applications.

Digital Computer Design: Logic, Circuitry, and Synthesis focuses on the logical structure, electronic realization, and application of digital information processors. The manuscript first offers information on numerical symbols, fundamentals of computing aids,

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quantization, representation of numbers in an electronic digital computer, and computer applications. The text then ponders on the nature of automatic computation and Boolean algebra. Discussions focus on the advantages of a Boolean algebraic description of a digital computer; clock pulse generators and timing circuits; sequential switching networks; elements of information processing systems and types of digital computers; and automatic sequencing methods. The book elaborates on circuit descriptions of switching and storage elements and large capacity storage systems. Topics include static magnetic storage, dynamic delay line storage, cathode-ray storage, vacuum tube systems of circuit logic, and magnetic core systems of

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circuit logic. The publication also examines the system design of GP computers, digital differential analyzer, and the detection and correction of errors. The text is a valuable source of data for mathematicians and engineers interested in digital computer design.

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